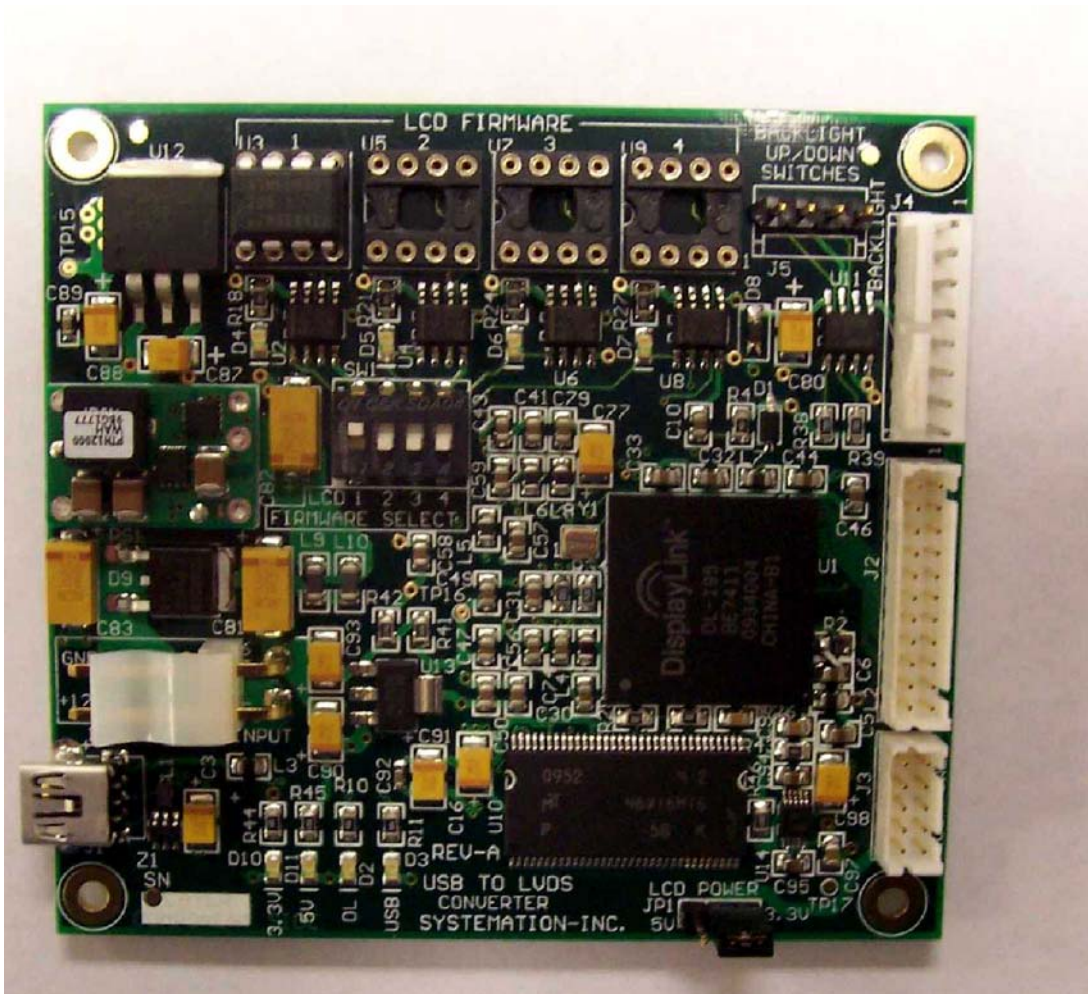


ADP-105 USB to LVDS Adapter Board

The Systemation ADP-105 Adapter Board converts USB 2.0 data from a Windows based computer into LVDS data to directly drive an LCD. The board uses a Windows driver from DisplayLink that runs on all Windows versions from Windows 2000 to Windows 7. This board supports either 1 or 2 channel LVDS outputs at resolutions up to 2560 x 1536.



S P E C I F I C A T I O N S

- 1 – Data Input: USB 2.0 using a 5-pin mini-USB connector
- 2 – Power Input: 12VDC using a 0.156 2-pin connector
- 3 – Backlight Control: An EEPROM digital Pot is used to provide an analog voltage to control the LCD backlight brightness. A 4-pin switch closure input steps the backlight brightness up and down.
- 4 – LCD Drive: A 22-pin 2mm and a 10-pin 2mm connection provide a “Systemation” type LVDS interface to drive most LVDS type LCDs.
A 3-pin jumper is used to select the power to the LCD from 3.3V or 5.0V
- 5 – LCD Support: The board can drive an LCD with resolution up to 2560x1536. Firmware on the board specifies the exact resolution of the LCD selected. Up to 4 EPROMs can be installed at a time.
- The firmware for each LCD type/resolution is created by Systemation.
- Demonstration ADP-105 board are populated with EPROMs for SVGA, XGA, SXGA and UXGA LCDs.
- Any LCD resolution is possible. Consult Systemation.
- 6 – LCD Support: 3.25” X 3.00”
Mounting holes on corners

7 – Cables:

LVDS Data Cables available for most LVDS
type LCDs.

8 – Software Driver:

A Windows driver for the ADP-105 board is available
from Systemation or from Displaylink.

<http://www.displaylink.com/support/index.php>

J1	USB Input			5-Pin Mini-USB
J1-1	USB 5V			
J1-2	USB D-			
J1-3	USB D+			
J1-4	Key (Not Used)			
J1-5	Ground			

J2	Description		MFG	MFG P/N
	LVDS Data Output		Hirose	DF11-22DP-2DSA
J2-1	Ain3+			
J2-2	Ain3-			
J2-3	Aclk+			
J2-4	Aclk-			
J2-5	Ain2+			
J2-6	Ain2-			
J2-7	Ain1+			
J2-8	Ain1-			
J2-9	Ain0+			
J2-10	Ain0-			
J2-11	Bin3+			
J2-12	Bin3-			
J2-13	Bclk+			
J2-14	Bclk-			
J2-15	Bin2+			
J2-16	Bin2-			
J2-17	Bin1+			
J2-18	Bin1-			
J2-19	Bin0+			
J2-20	Bin0-			
J2-21	VCC-SW			
J2-22	Ground			

VCC-SW is selected by JP1

J3	Description		MFG	MFG P/N
	LVDS LCD POWER		Hirose	DF11-10DP-2DSA
J3-1	12VDC	VCC-SW is selected by JP1		
J3-2	Ground			
J3-3	12VDC			
J3-4	Ground			
J3-5	VCC-SW			
J3-6	Ground			
J3-7	VCC-SW			
J3-8	Ground			
J3-9	VCC-SW			
J3-10	Ground			

J4	Inverter Interface		Molex	22-23-2081
J4-1	+12VDC Switched			
J4-2	+12VDC Switched			
J4-3	+5VDC Switched			
J4-4	+5VDC Switched	Mating Hardware	MFG	MFG P/N
J4-5	Not Used	Shell	Molex	22-01-3087
J4-6	Not Used	Crimp	Molex	08-52-0123
J4-7	GND			
J4-8	GND			

J5	Inverter Interface		Molex	22-23-2041
J5-1	Brightness Up Switch			
J5-2	Ground			
J5-3	Brightness Down Switch			
J5-4	Ground	Mating Hardware	MFG	MFG P/N
		Shell	Molex	22-01-3047
		Crimp	Molex	08-52-0123

J6	Power Connector		Molex	26-65-5020
J6-1	+12VDC			
J6-2	GND	Mating Hardware	MFG	MFG P/N
		Shell	Molex	09-50-8023
		Crimp	Molex	08-52-0072

Systemation Technology
1321 3rd Street League
City, Texas 77573
Tel 281.332.7187 / Fax 281.332.7188
www.systemation-inc.com
support@systemation-inc.com